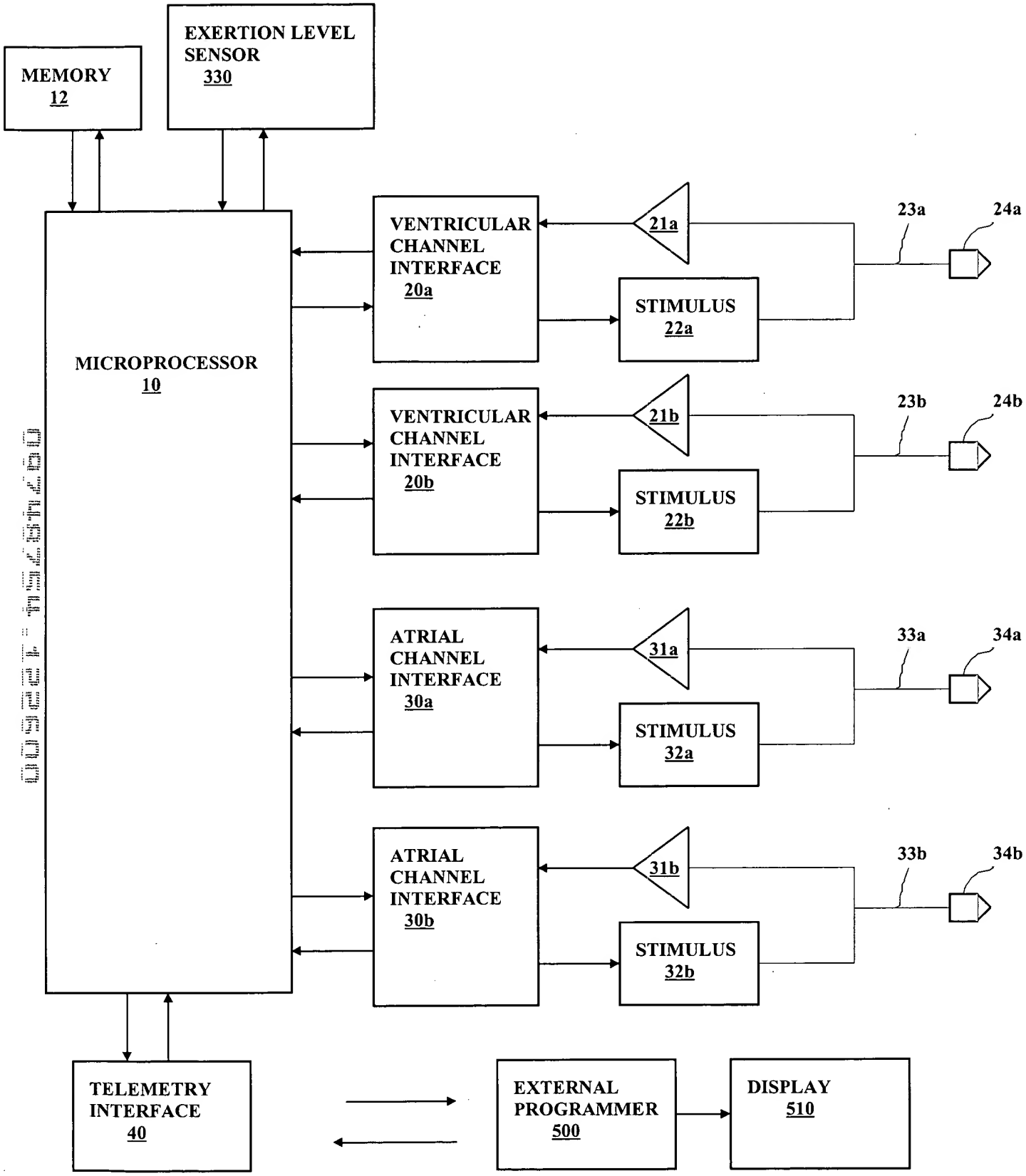
MEMORY
12
EXERTION LEVEL SENSOR
330
MICROPROCESSOR
10
VENTRICULAR CHANNEL INTERFACE
20a
21a
STIMULUS
22a
23a
24a
VENTRICULAR CHANNEL INTERFACE
20b
21b
STIMULUS
22b
23b
24b
ATRIAL CHANNEL INTERFACE
30a
31a
STIMULUS
32a
33a
34a
ATRIAL CHANNEL INTERFACE
30b
31b
STIMULUS
32b
33b
34b
TELEMETRY INTERFACE
40
EXTERNAL PROGRAMMER
500
DISPLAY
510

FIG. 1

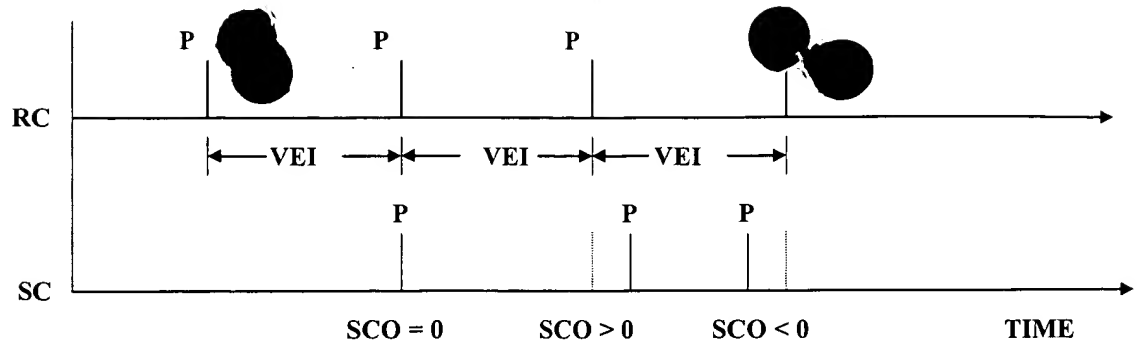

FIG. 2

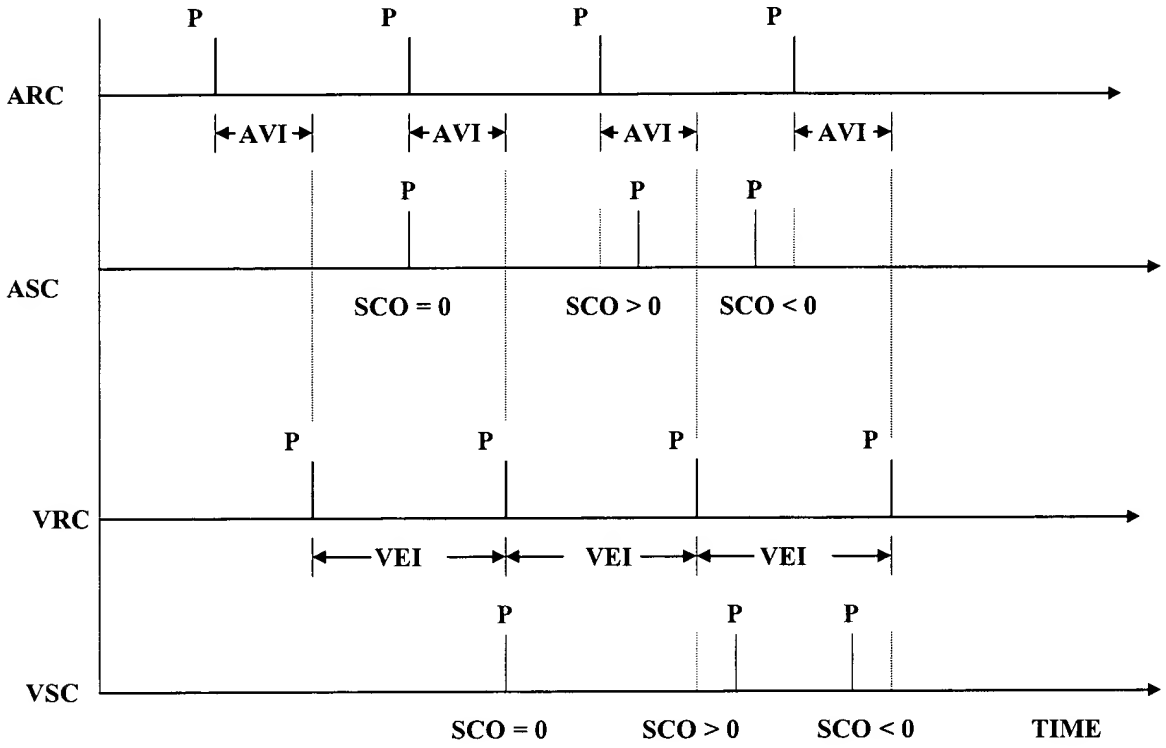

FIG. 3

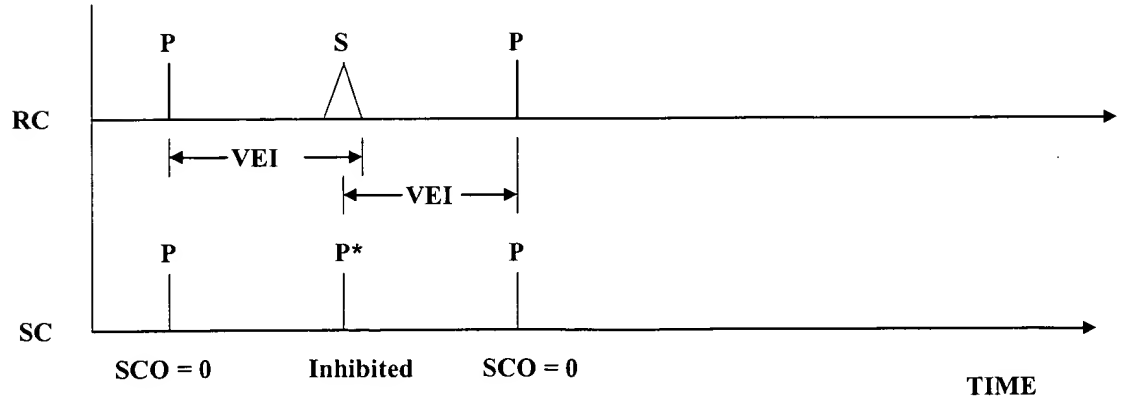

FIG. 4

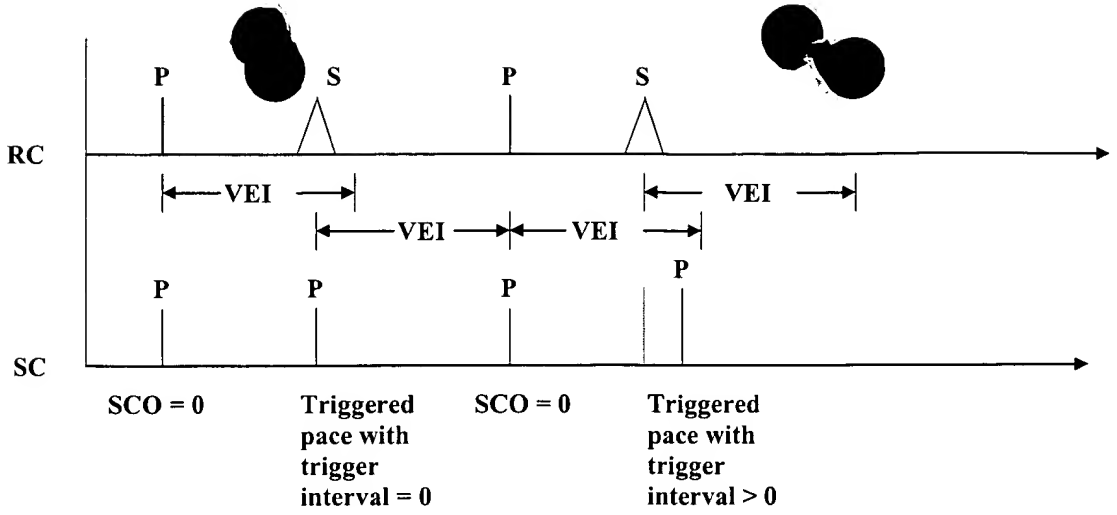

FIG. 5A

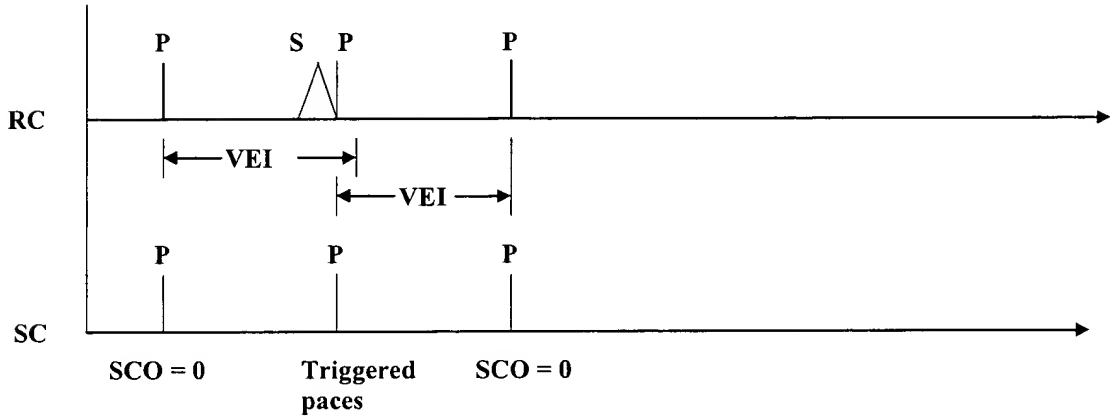

FIG. 6

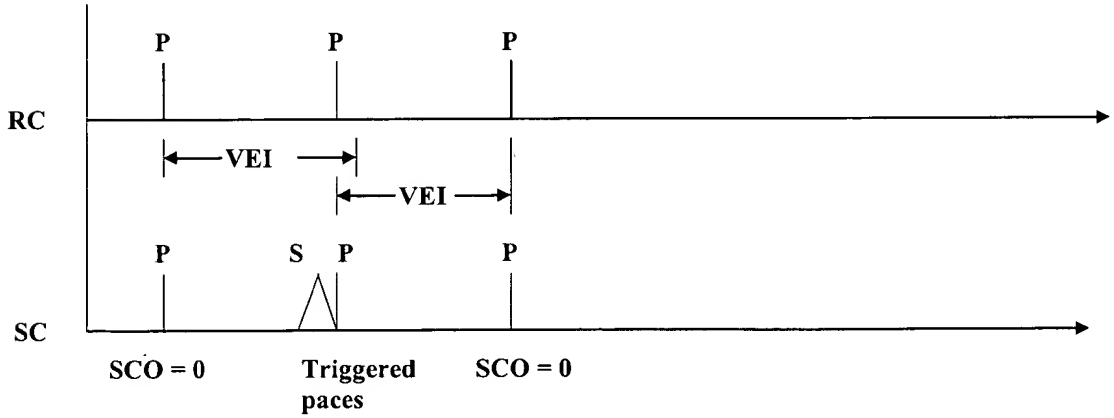

FIG. 7

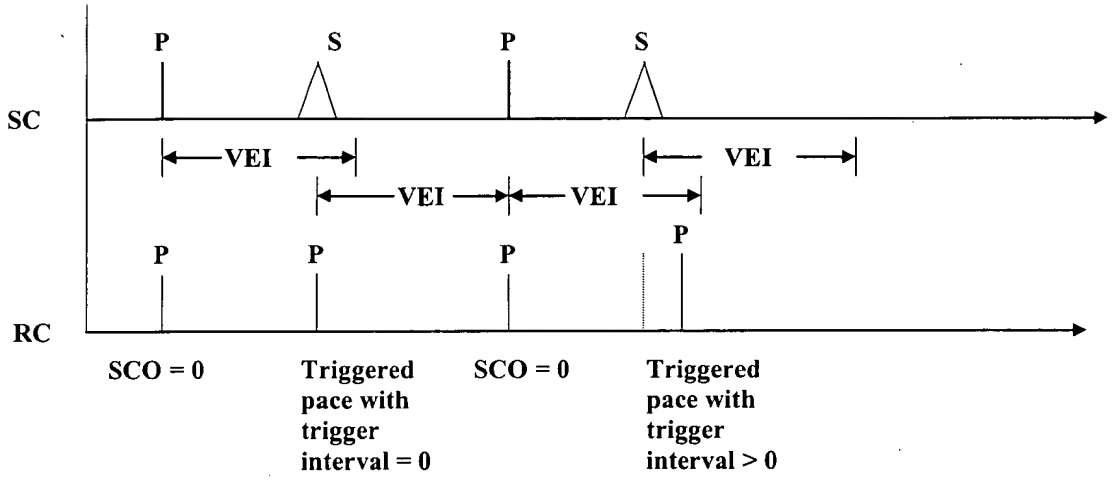
P
S
P
S
SC
VEI
VEI
VEI
VEI
P
P
P
P
RC
SCO = 0
Triggered pace with trigger interval = 0
SCO = 0
Triggered pace with trigger interval > 0

FIG. 5B

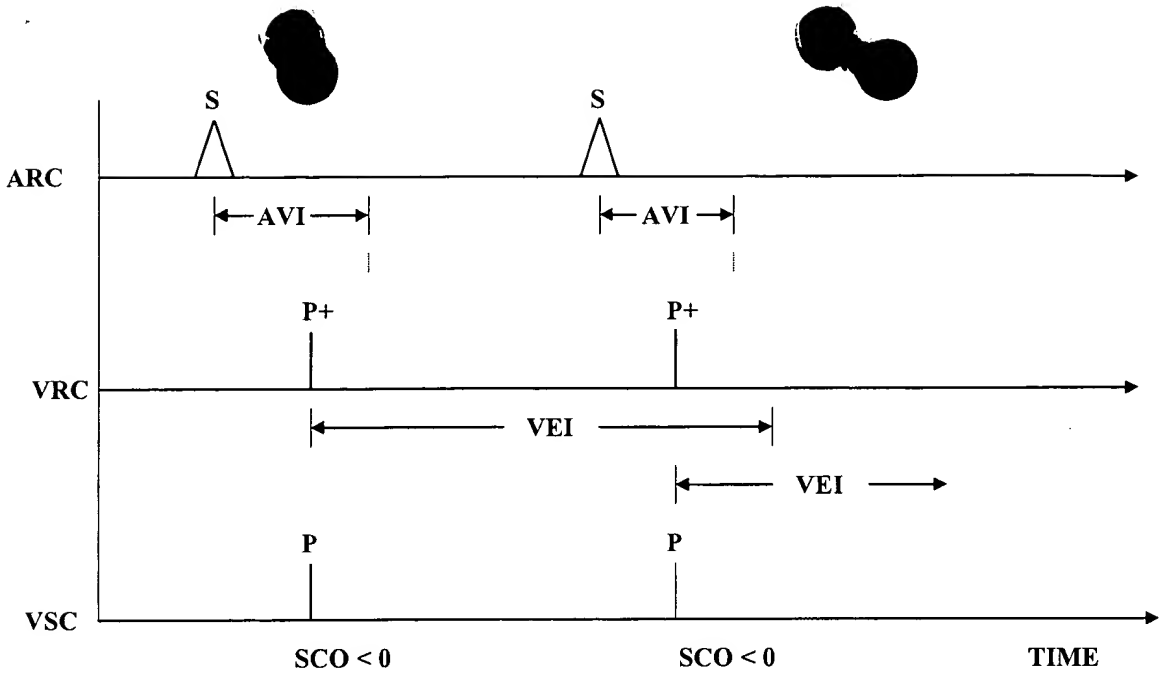
S
S
ARC
AVI
AVI
P+
P+
VRC
VEI
VEI
P
P
VSC
SCO < 0
SCO < 0
TIME

FIG. 8B

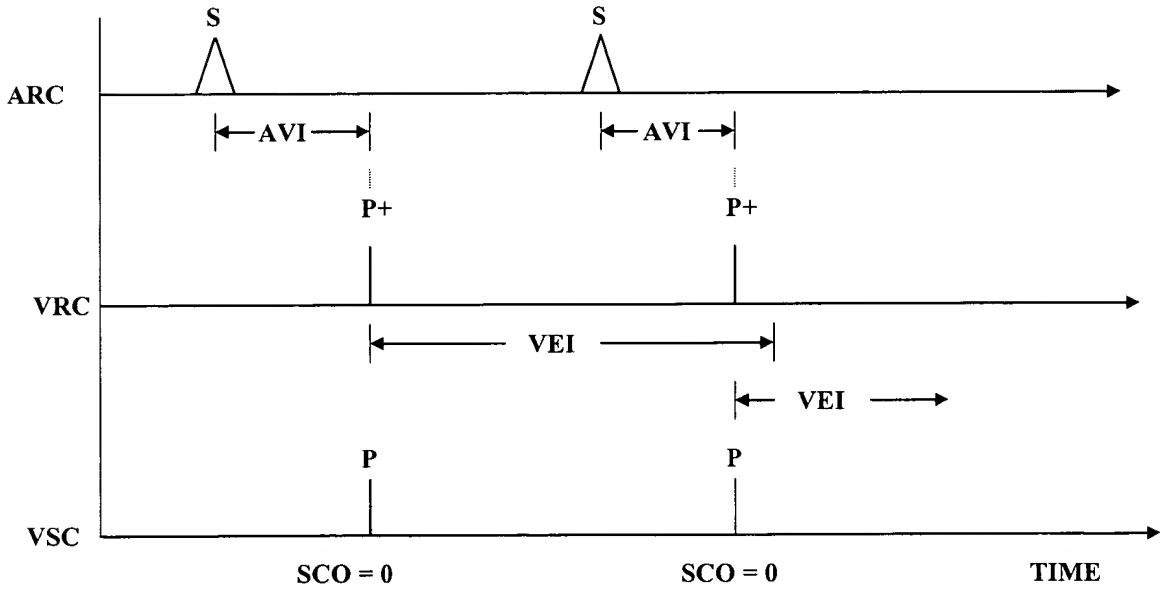
S
S
ARC
AVI
AVI
P+
P+
VRC
VEI
VEI
P
P
VSC
SCO = 0
SCO = 0
TIME

FIG. 8A

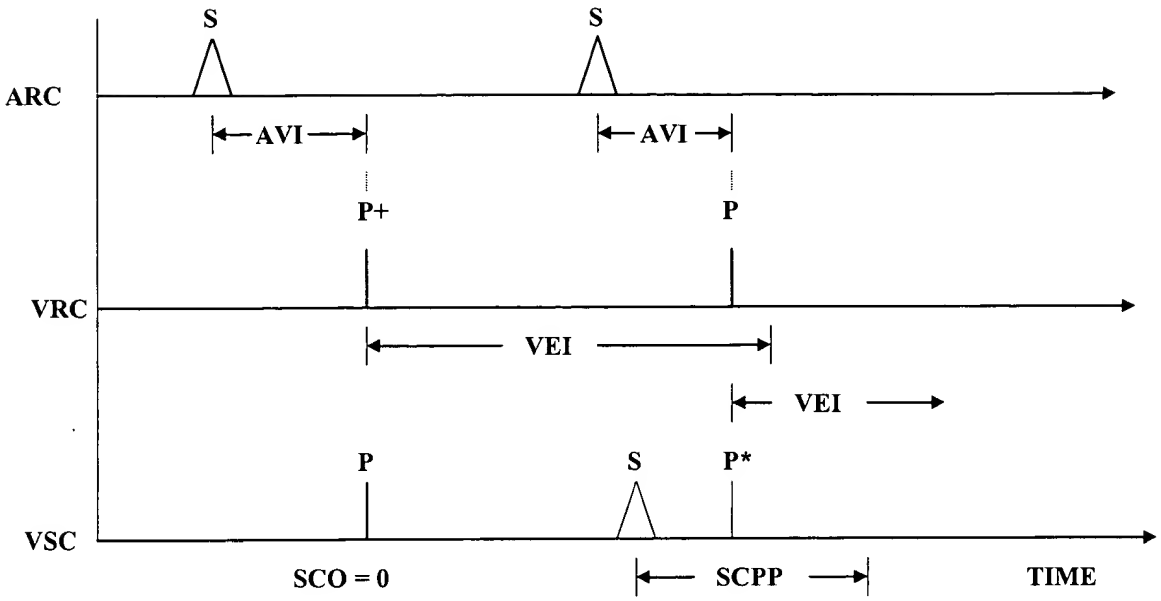

FIG. 8C

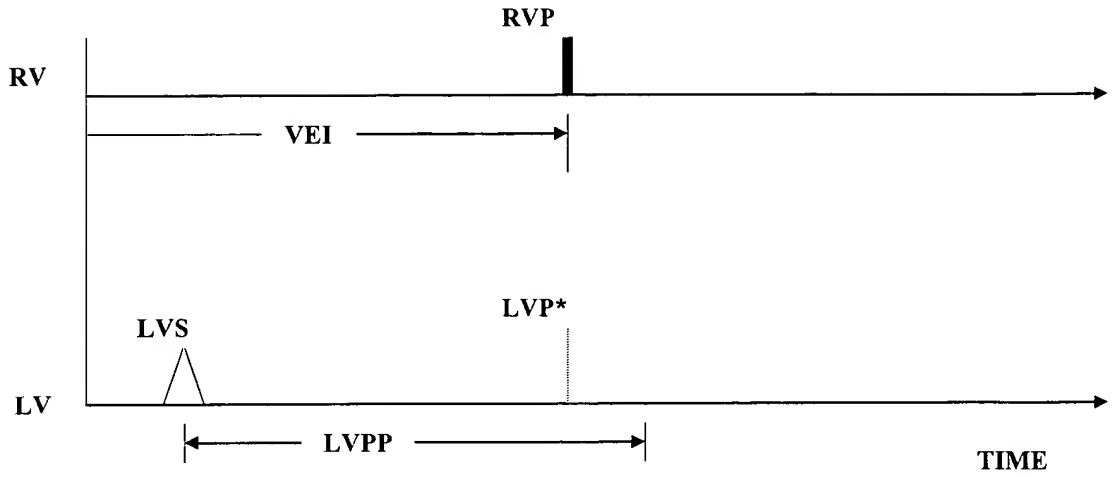

FIG. 9

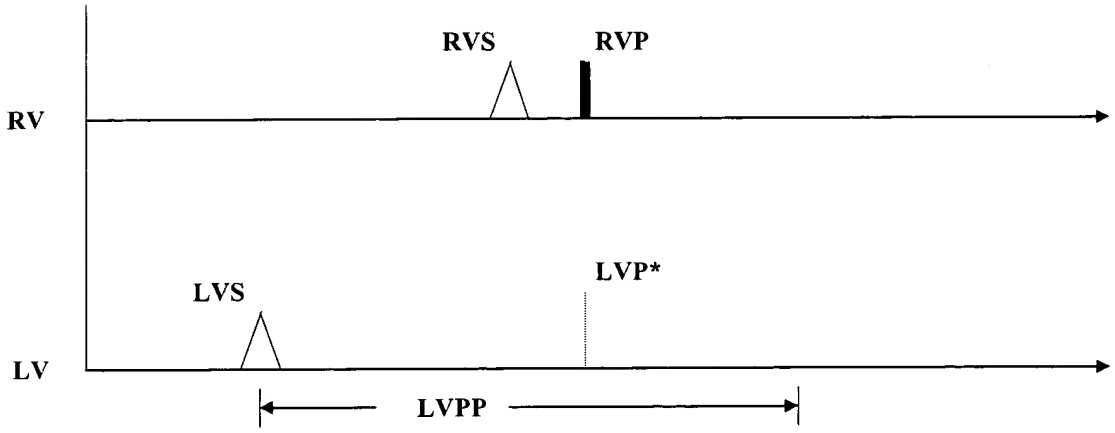

FIG. 10